

ABSTRACT OF THE DISCLOSURE

An array substrate for use in an in-plane switching liquid crystal display device includes a plurality of gate lines having a first direction on a substrate; a plurality of data lines having a second direction substantially perpendicular to the plurality of gate lines, wherein pairs of the gate and data lines define a pixel region; a common line in the first direction between the plurality of gate lines; a plurality of common electrodes extending from the common line in the pixel region, wherein the common electrodes have an arc shape; thin film transistors disposed at four corners of the pixel region near crossings of the gate and data lines; a capacitor electrode above a portion of the common electrodes, wherein the capacitor electrode is connected to a corresponding thin film transistor; and a plurality of pixel electrodes connected to the capacitor electrode and disposed between the arc shape of the common electrodes, wherein the pixel electrodes are arc shaped, wherein the pixel region is divided into a plurality of sub pixels, each of which includes one thin film transistor, at least one capacitor electrode, and at least one pixel electrode.